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Technology Center 2100

TOWNSEND and TOWNSEND and CREW LLP

Bothin L. Mr. Keill-

rember 12, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ashley Saulsbury, et al.

Application No.: 09/802,017

Filed: March 8, 2001

For: VLIW COMPUTER PROCESSING ARCHITECTURE WITH ON-CHIP DRAM USABLE AS PHYSICAL MEMORY OR CACHE MEMORY

Customer No.: 20350

Confirmation No.: 4713

Examiner:

Portka, Gary J.

Technology Center/Art Unit: 2188

AMENDMENT

Mail Stop Non-Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 5, 2003, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.